


Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office INFORMATION DISCLOSURE STATEMENT BY APPLICANT 	Honeywell Ref. No. H0002270 DIV MBHB Case No. 06-308-E	Serial No. 10/764,938
	Applicant: Fathimulla et al.	
	Filing Date: January 26, 2004	Group: 2814

U.S. PATENT DOCUMENTS

Examiner Initial	No.	Document Number	Date	Name	Class	Subclass	Filing Date

FOREIGN PATENT DOCUMENTS

Examiner Initial	No.	Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

OTHER DOCUMENTS - Including Author, Title, Date, Pertinent Pages, Etc.

Examiner Initial	No.	
LP	1.	A.B Glaser et al., "Integrated Circuit Engineering," p. 265-267, 289 Addison-Wesley, 1977
LP	2.	U.S. Davidsohn and F. Lee., "Dielectric-Isolated Integrated-Circuit Substrate Processes." Proc. IEEE 57: 1532-1537 (September 1969).

Examiner	L. Pham	Date Considered	5/9/07
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